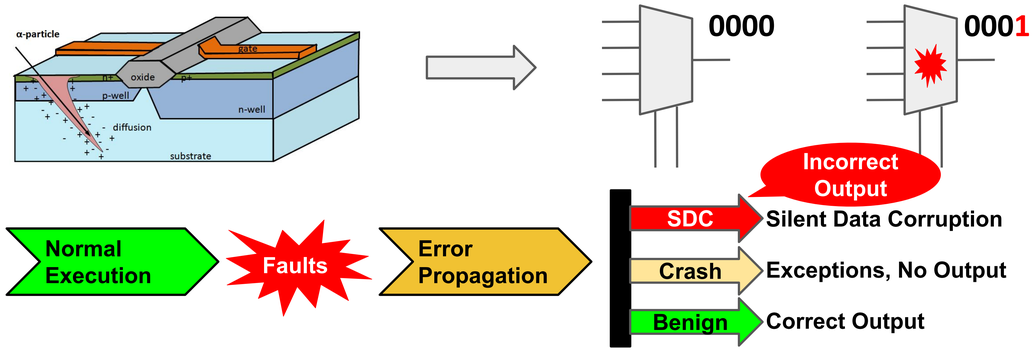
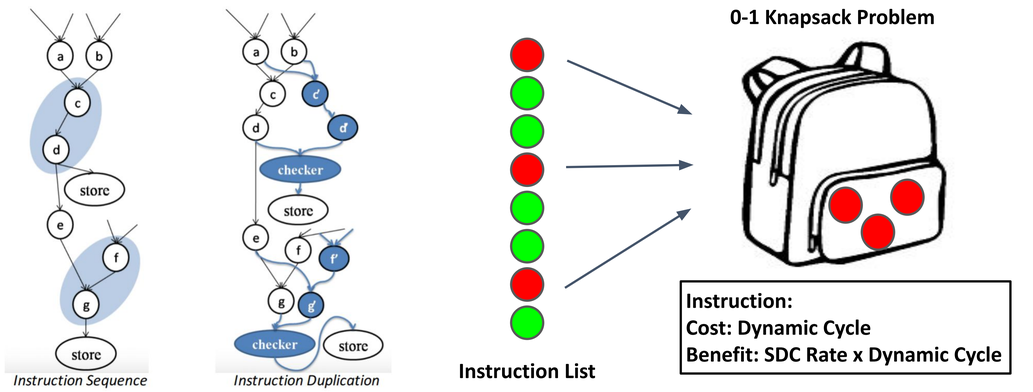
POSTER: Hardening Selective Protection across Multiple Inputs for HPC Applications

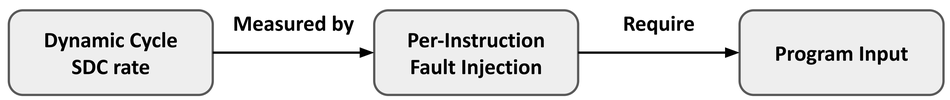
Motivation: Soft Errors



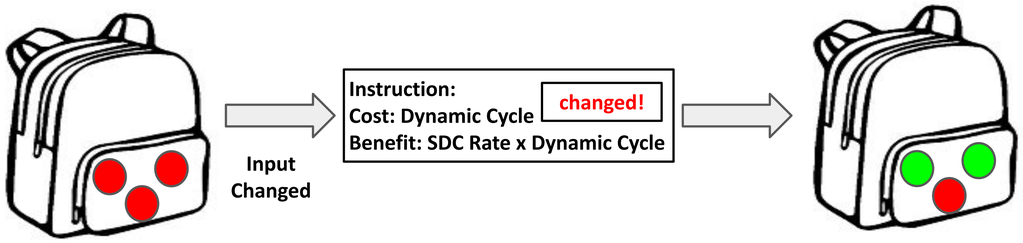
Background: Selective Instruction Duplication



Problem



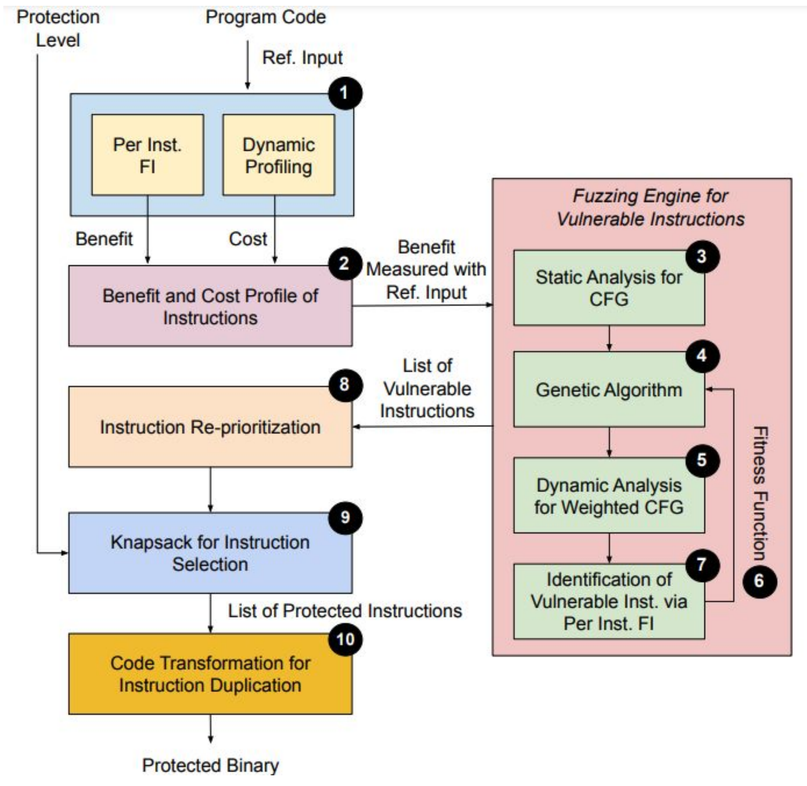
- When program input changes, per-instruction dynamic cycle and SDC rate will also change!
- Time cost of fault injection is huge! Can not repeat.



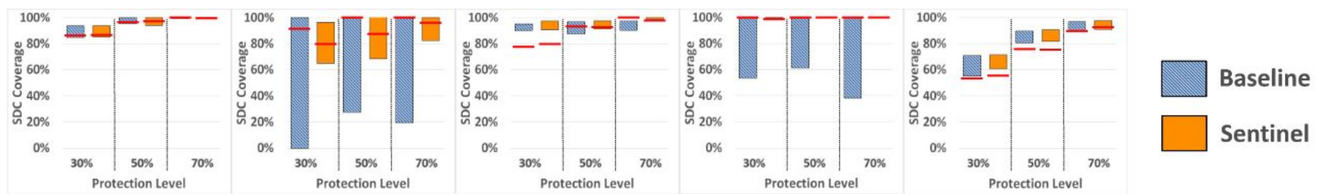
The protection is not effective anymore!

Solution: SENTINEL

- Goal:
  - Protecting programs under arbitrary inputs.
- Key Insight:
  - Only a small set of instructions drastically changes benefits while changing inputs. We call them *vulnerable instructions*.
- Key Idea of Workflow:
  - Fuzzing technique to automatically detect *vulnerable instructions*.
  - Instruction re-prioritization. Forcibly protecting *vulnerable instructions*.
  - Code transformation and compile.



Evaluation



(a) Pathfinder (b) Needle (c) Particlefilter (d) kNN (e) HPCCG

Benchmark	30% Level	50% Level	70% Level
Pathfinder	13.33%	3.33%	3.33%
Needle	3.33%	3.33%	3.33%
Particlefilter	0.00%	56.67%	0.00%
kNN	0.00%	0.00%	0.00%
HPCCG	0.00%	0.00%	3.33%
Average	3.33%	12.67%	2.00%
Average (Baseline)	23.33%	42.67%	44.67%

- SENTINEL can significantly reduce the SDC coverage variation among arbitrary inputs.
- More inputs can reach the expected coverage.

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